



09/884 778

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**TRANSMITTAL LETTER**  
**(General - Patent Issued)**Docket No.  
**END920000187US1**Patentee(s) **Hall et al.**

U.S. Patent No.

**6,982,387 B2**

Issue Date

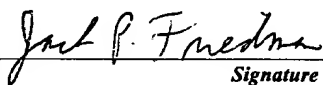
**January 3, 2006**Title: **METHOD AND APPARATUS TO ESTABLISH CIRCUIT LAYERS INTERCONNECTIONS**COMMISSIONER FOR PATENTS:

Transmitted herewith is:


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**of Correction**

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**FEB 01 2006**

P17A/REV04

# UNITED STATES PATENT AND TRADEMARK OFFICE

## CERTIFICATE OF CORRECTION

PATENT NO. : 6,982,387 B2

DATED : January 3, 2006

INVENTOR(S) : Hall et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the Abstract

Line 3, delete "that portion" and insert -- that a portion--

Column 4

Line 63, delete "wherein upper" and insert -- wherein the upper --

MAILING ADDRESS OF SENDER (Please do not use customer number)

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PATENT NO. 6,982,387 B2

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P15/REV03  
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